# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of )

Hosein, et al. )
)
Serial No.: 10/731,909 )
)
Filed: December 10, 2003 )
)

For: **PER USER RATE CONTROL FOR THE** )
**REVERSE LINK IN CDMA NETWORKS** )
)
Attorney's Docket No.: 4740-231 )

Examiner <u>TBA</u>
Group Art Unit <u>TBA</u>

Raleigh, North Carolina
May 6, 2004

Mail Stop Patent Applications
Commissioner for Patents
P.O. Box 1450
Alexandria, VA   22313-1450

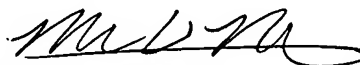## Information Disclosure Statement

Sir:

Attached hereto is an information disclosure statement (PTO-1449). This IDS is being

submitted before any action on the merits. As such, no fee is required. However, if fees are

required, please deduct from our Deposit Account No. 18-1167.

Respectfully submitted,

**COATS AND BENNETT, PLLC**

By:

Michael D. Murphy
Registration No. 44,958

Telephone: (919) 854-1844

**FORM PTO-1449**

**INFORMATION DISCLOSURE CITATION IN AN APPLICATION PURSUANT TO 37 CFR §1.97 & 1.98**

| Docket Number: 4740-231 | Serial Number: 10/731,909 |
|---|---|

| Applicant: Hosein, et al. | |
|---|---|

| Filing Date: December 10, 2003 | Group: |
|---|---|

## U. S. PATENT DOCUMENTS

| Examiner Initial | | Patent No. | Date | Name | Class | Subcl | Filing Date If Approp. |
|---|---|---|---|---|---|---|---|
| | | | | | | | |
| | | | | | | | |

## FOREIGN PATENT DOCUMENTS

| | Patent No. | Date | Name | Class | Subcl | Translation Yes | No |
|---|---|---|---|---|---|---|---|
| | | | | | | | |
| | | | | | | | |

## OTHER DOCUMENTS (including author, title, date, pages, etc.)

| | | |
|---|---|---|
| | A | Lucent Technologies; Farooq Khan; System Performance of Lucent's HRPD Rev. A RL Proposal; C30-20030915-217; 3 pgs. |
| | B | Samsung Electronics; Kwon, et al.; Description of Scheduling and Resource Allocation for Various Control Mechanisms simulated by Samsung under HARQ Scenario; C30-WG3-20030812-071; 14 pgs. |
| | C | Avinash Jain; Full Buffer Performance of 5ms, 4 transmission and 10ms, 3 transmission with distributed scheduling using geometry based-priority function; August 12 2003; C30-20030812-061-QCOM; 4 pgs. |
| | D | LG Electronics; Kim, et al.; Rate Control Mechanism for LGE's Reverse Link Framework Proposal for Revision D; August 6, 2003; C30-2203-806-021; 9 pgs. |
| | E | Nokia; Pi, et al.; Cdma2000 Rev.D Performance with 10ms 3 HARQ Transmission and Full Buffer Traffic; August 6, 2003; C30-20030806-042; 9 pgs. |
| | F | Nortel Networks; Vrzic, et al.; Full Buffer Simulation Results for Semi-distributed Scheduling, with 10ms Frame and 3 Transmissions; C30-2003-806-052R1;2 pgs. |

| EXAMINER | DATE CONSIDERED |
|---|---|
| | |

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation not in conformance and not considered. Include copy of this form with next communication to the applicant.